



B

Docket No.: T2171.0180/P180
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Takahisa Yamaha

Application No.: 09/518,709 ✓

Allowed: June 25, 2004

Filed: March 3, 2000

Art Unit: 2815

For: MANUFACTURE METHOD FOR
SEMICONDUCTOR WITH SMALL
VARIATION IN MOS THRESHOLD
VOLTAGE

Examiner: E. Ortiz

SUBMISSION OF FORMAL DRAWINGS

MS PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (one sheet, two figures – Figures 6-7) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the drawings submitted with the originally filed application.

Dated: August 6, 2004

Respectfully submitted,

By Steven I. Weisburd
Steven I. Weisburd
Registration No.: 27,409
DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP
1177 Avenue of the Americas
41st Floor
New York, New York 10036-2714
(212) 835-1400
Attorney for Applicant